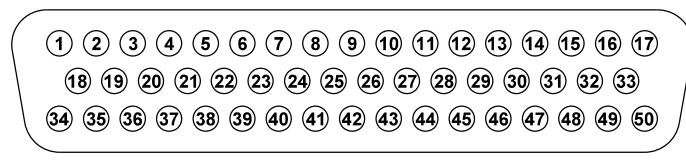
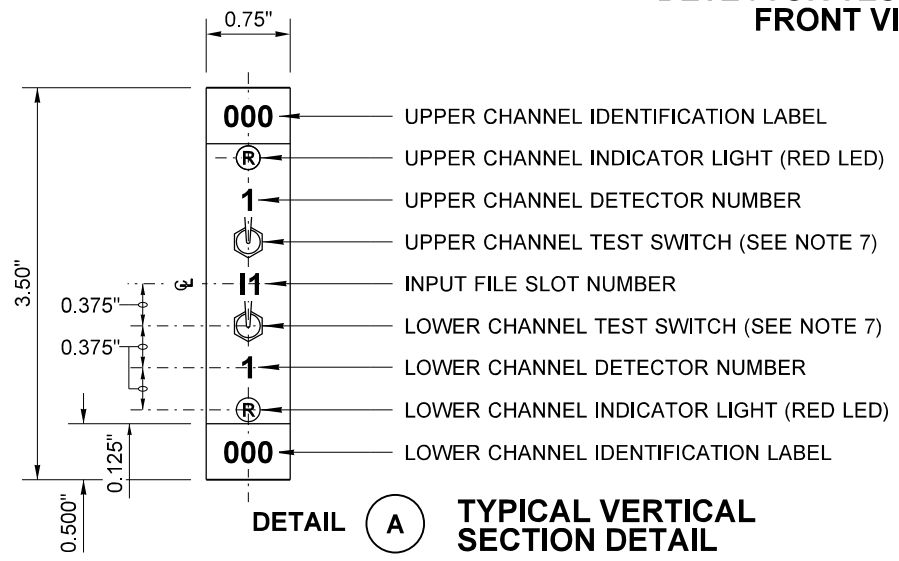


SEE DETAIL A
TYPICAL VERTICAL SECTION DETAIL

SEE NOTES 7 AND 8

DETECTOR TEST PANEL FRONT VIEW

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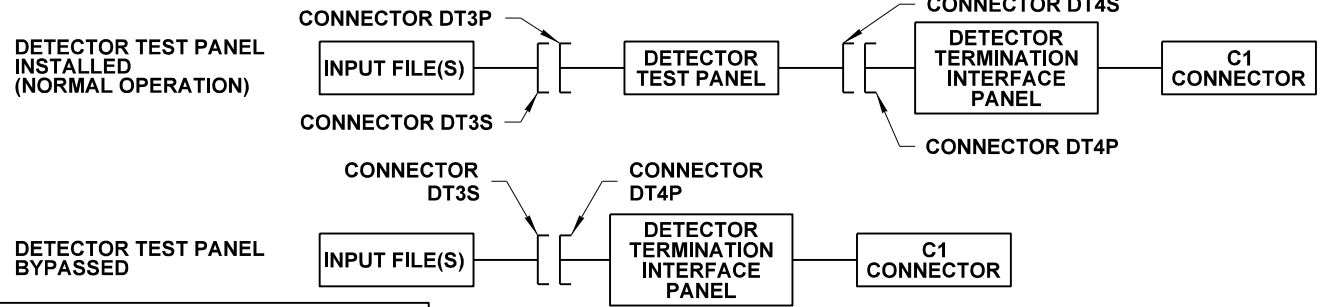


DD50 D-SUB CONNECTOR PINS
PLUG (MALE) CONNECTOR SHOWN ~
MIRROR FOR SOCKET (FEMALE) CONNECTOR ~
SEE NOTE 2

NOTES

- Upper and lower channel identification labels shall match the detector channels shown in the Contract Plans.
- Connectors DT3S, DT3P, DT4S, and DT4P are Type DD50 D-Sub connectors with pin layouts and assignments as shown. The suffix "S" indicates a socket (female connector) and the suffix "P" indicates a plug (male connector).
- Detector Termination Interface Panel** terminals not shown due to variations in arrangement and numbering between manufacturers.
- Connectors DT3P and DT4S shall be installed in one of the following arrangements:
 - Mounted to the back of the **Detector Test Panel**. Connectors shall use a spring latch (bail) to secure the connection.
 - Mounted on a cable, within six inches of the back of the **Detector Test Panel**. Connectors shall use thumb-screws to secure the connection.
- Connectors DT3S and DT4P shall be designed such that they can be connected directly, bypassing the **Detector Test Panel**.
- The **Detector Termination Interface Panel** shall be installed electrically between the **Detector Test Panel** and the C1 connector. A second additional terminal block may be installed electrically between the **Input File(s)** and the **Detector Test Panel**.
- Test switches shall be three position switches with the "Test" position being a momentary contact with spring return to the "OFF" position. Test switch position functions shall be as described in **Standard Specification section 9-29.13(10)**.
- Location of the **Display On/Off** switch is approximate. This switch shall be located to the right of all of the individual channel test switches and clear of the mounting rack.

FUNCTIONAL BLOCK DIAGRAMS



CONNECTOR PIN ASSIGNMENTS (SEE NOTE 3)

CONNECTOR DT3S			CONNECTOR DT3P			CONNECTOR DT4S			CONNECTOR DT4P		
PIN	CONNECT TO	FUNCTION	PIN	CONNECT TO	FUNCTION	PIN	CONNECT TO	FUNCTION	PIN	CONNECT TO	FUNCTION
1	I-1F	DET. 1	26	J-3F	DET. 23	1	I1U - IN		26	J3U - OUT	
2	I-2F	DET. 3	27	J-4F	DET. 25	2	I2U - IN		27	J4U - OUT	
3	I-3F	DET. 5	28	J-5F	DET. 27	3	I3U - IN		28	J5U - OUT	
4	I-4F	DET. 7	29	J-2W	DET. 22	4	I4U - IN		29	J2L - OUT	
5	I-1W	DET. 2	30	J-3W	DET. 24	5	I1L - IN		30	J3L - OUT	
6	I-2W	DET. 4	31	J-4W	DET. 26	6	I2L - IN		31	J4L - OUT	
7	I-3W	DET. 6	32	J-5W	DET. 28	7	I3L - IN		32	J5L - OUT	
8	I-4W	DET. 8	33	J-6F	DET. 29	8	I4L - IN		33	J6U - OUT	
9	I-5F	DET. 9	34	J-7F	DET. 31	9	I5U - IN		34	J7U - OUT	
10	I-6F	DET. 11	35	J-8F	DET. 33	10	I6U - IN		35	J8U - OUT	
11	I-7F	DET. 13	36	J-9F	DET. 35	11	I7U - IN		36	J9U - OUT	
12	I-8F	DET. 15	37	J-6W	DET. 30	12	I8U - IN		37	J6L - OUT	
13	I-5W	DET. 10	38	J-7W	DET. 32	13	I5L - IN		38	J7L - OUT	
14	I-6W	DET. 12	39	J-8W	DET. 34	14	I6L - IN		39	J8L - OUT	
15	I-7W	DET. 14	40	J-9W	DET. 36	15	I7L - IN		40	J9L - OUT	
16	I-8W	DET. 16	41	NC	NA	16	I8L - IN		41	NC	NA
17	I-9F	DET. 17	42	NC	NA	17	I9U - IN		42	NC	NA
18	I-12F	Ø2 PED	43	NC	NA	18	I12U - IN		43	NC	NA
19	I-13F	Ø6 PED	44	NC	NA	19	I13U - IN		44	NC	NA
20	J-1F	DET. 19	45	NC	NA	20	J1U - IN		45	NC	NA
21	I-9W	DET. 18	46	NC	NA	21	I9L - IN		46	NC	NA
22	I-12W	Ø4 PED	47	NC	NA	22	I12L - IN		47	NC	NA
23	I-13W	Ø8 PED	48	NC	NA	23	I13L - IN		48	NC	NA
24	J-1W	DET. 20	49	I15-1	POWER	24	J1L - IN		49	NC	NA
25	J-2F	DET. 21	50	I15-2	GROUND	25	J2U - IN		50	NC	NA

PIN TABLE EXAMPLES:
J1F: Input File J, Slot 1, Terminal F
DET. 14: Detector #14
I9U - IN: Detector Test Panel Position I9, Upper Channel, Input Terminal
C1 - 58: C1 Connector, Pin 58
N/A: Not Applicable
NC: Not Connected



**TYPE 332 SIGNAL CABINET
DETECTOR TEST PANEL
STANDARD PLAN J-80.15-00**

SHEET 1 OF 1 SHEET

APPROVED FOR PUBLICATION